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To be disclosed...

Final Call for Papers

SoC 2013

The International Symposium on System-on-Chip
Tampere, Finland October 23-24, 2013

Organizer: Tampere Univ. of Tech. / Dept. of Electronics and Communications Engineering
Technical co-sponsor: IEEE Circuits and Systems Society
Supported by: Nokia Corporation (others to be confirmed)

SoC 2013 is the 15th SoC event, building on the tradition of a series of SoC events organized in Tampere, Finland annually since 1999. It consists of invited talks, contributed scientific papers, special sessions, and a commercial exhibit with its own industrial program track. The symposium also features a panel discussion on topics of high interest to the System-on-Chip community, and a pre-conference tutorial on October 22. The mission of SoC 2013 is to provide a forum that is fully and comprehensively dedicated to SoC.

We encourage your contribution to the program by submitting a research paper, suggesting a distinguished invited lecturer and/or topic, submitting an abstract to the industry program track, suggesting a tutorial course, special session, or a topic and tentative participants for a panel discussion.

Topics of interest include, but are not limited to:

- **SS: Reconfigurable architectures**
- **SS: Network-based embedded computing**
- *Alternative computing concepts*
- *Advanced platform architectures*
- *Analysis and early estimation techniques*
- *Application-specific processors and architectures*
- *Hardware/software co-design and reuse techniques*
- *Low-power hardware and software techniques*
- *Operating systems for embedded systems*
- *Multiprocessor SoC implementations*
- *Network-on-chip and inter-chip communications*
- *Processor and compiler generators*
- *Retargetable compilers*
- *SoC verification and testing*
- *Software runtime environments, SW emulation and simulation*
- *Software tools and languages for multiprocessor systems*
- *Special SoC applications*
- *System-level design flow and methodology*

All topics should be handled highlighting the relevance to SoC.

Author Schedules:

Special session proposals, invited lectures, tutorials: March 24, 2013.

Scientific papers: ~~May 17, 2013~~ **June 2, 2013 (extended)**

Notification of acceptance for scientific papers: June 28, 2013.

Industrial abstracts, panels, camera-ready papers: August 16, 2013.

Notification of acceptance for industrial abstracts: September 6, 2013.

Paper submissions must be made electronically in A4 PDF format.

Additional submission instructions are available on the conference website.

The scientific papers will be published in the proceedings of SoC and in IEEE

Xplore. Best papers will be invited to be extended and submitted to IEEE

Design & Test of Computers and International Journal of Embedded and Real-

Time Communication Systems.

Complete details on the symposium website: <http://soc.cs.tut.fi/>

